

RECEIVER CIRCUIT, SIGNAL TRANSMISSION SYSTEM, AND
RECEIVER CIRCUIT DEVICE USED FOR HIGH-SPEED
SIGNAL TRANSMISSION

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ABSTRACT OF THE DISCLOSURE

10 A receiver circuit has a sampling circuit, a buffer
circuit, a determining circuit, and a buffer control
circuit. The sampling circuit samples an input signal,
and the buffer circuit buffers an output of the sampling
15 circuit. The determining circuit determines an output of
the buffer circuit, and the buffer control circuit keeps
a small input signal dependency of the output of the
buffer circuit until carrying out the sampling.
Consequently, an inter symbol interference caused by
20 characteristics of a transmission path which poses a
problem for receiving a high-speed signal can be
invalidated, and therefore the high-speed received signal
can be determined with a higher accuracy.